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	Application No.	Applicant(s)	
Notice of Allowability	10/002,748	SAITOH, SATORU	
	Examiner	Art Unit	
	Andrew C. Lee	2616	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this a or other appropriate communication B GHTS. This application is subject	pplication. If not included on will be mailed in due c	d ourse. THIS
1. This communication is responsive to <u>11/15/2001</u> .			
2. X The allowed claim(s) is/are <u>1-7</u> .			
 3. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have linternational Bureau (PCT Rule 17.2(a)). 	e been received. e been received in Application No.		on from the
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		y complying with the requ	uirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv			OTICE OF
 CORRECTED DRAWINGS (as "replacement sheets") mu (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 2 each sheet. Replacement sheet(s) should be labeled as such in 	son's Patent Drawing Review(PTC - 's Amendment / Comment or in the 1.84(c)) should be written on the dray	Office action of vings in the Iront (not the I	back) of
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT 			ote the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal	Patent Anniestica	
 Notice of References Cited (PTO-692) Dotice of Draftperson's Patent Drawing Review (PTO-948) 	-	• •	
	Paper No./Mail D	ate	
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date <u>11/15/2001</u> 	7. Examiner's Amen	umenvomment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ☐ Examiner's Stater	nent of Reasons for Allov	vance

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DETAILED ACTION

Allowable Subject Matter

1. Claims 1-7 are allowed over prior art.

The following is an examiner's statement of reasons for allowance:

Regarding claim 1, the prior art of record, in single or in combination, does not disclose explicitly the limitation of M memory banks capable of being accessed. simultaneously; and a data access unit for accessing said memory banks in each memory cycle having a predetermined length, according to a predetermined number N or less of read commands and write commands; a write requesting unit for dividing each reception packet into at least one data block according to a predetermined data length, and for issuing write requests for the respective data blocks; a write-bank selecting unit for selecting different memory banks in response to N+1 consecutive write requests, respectively; a data writing unit for selecting, in each memory cycle, maximum N write requests from said write requests issued by said write requesting unit, the selecting being performed in issuance order, and for inputting, to said data access unit, a write command to the effect that each of data blocks corresponding to the selected write requests is to be written to said memory banks selected by said write-bank selecting unit; an address managing unit for managing information about said memory banks, and addresses of their storage areas where respective data blocks of each packet have been written; a primary read-bank selecting unit for receiving, in each memory cycle, read requests for N or less data blocks from said scheduler, and for selecting one or more memory bank(s) capable of being read in the same memory cycle from memory

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banks in which the data blocks specified by the respective read requests are stored, based on said information stored in said address managing unit; a secondary read-bank

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selecting unit for storing a read request which has not been achieved in each memory

cycle, the read request being among read requests received in the same memory cycle

from said scheduler, and for selecting a memory bank corresponding to the stored read

request in the next memory cycle; and a data reading unit for selecting a predetermined

number L or less of read requests from said read requests for the memory banks

selected by said primary and secondary read-bank selecting units, and for inputting, to

said data access unit, L or less read commands to the effect that respective data blocks

are to be read from the memory banks corresponding to said read requests.

2. Additionally, all of the further limitations in claims 2-7 are allowable since the

claims are dependent upon the independent claim.

Any comments considered necessary by applicant must be submitted no later

than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

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- Chao et al. (US 6370144 B1) disclose congestion control and traffic
 management in networks and inter-networks operating at relatively high data
 rates and carrying information which may have differing quality of service (or
 "QoS") requirements.
- Herbst (US 6792500 B1) discloses a method and apparatus for managing defects in a memory, wherein the method includes the steps of testing a plurality of memory locations to determine an inoperable memory location and moving a memory address corresponding to the inoperable memory location to a first position in a list of available memory addresses.
- Wilford et al. (US 6687247 B1) disclose a line-card architecture for high speed routing of data in a communications device.
- Herbst et al. (US 6735679 B1) teach a method and apparatus for optimizing
 access to memory, wherein the method includes the steps of receiving a first
 request for access to a memory, receiving at least two additional requests for
 access to the memory, and determining a first clock overhead associated
 with the first request for access to the memory.
- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew C. Lee whose telephone number is (571) 272-3131. The examiner can normally be reached on Monday through Friday from 8:30am 5:00pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wing Chan can be reached on (571) 272-7493. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/ACL/

Jan 04, 2007

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